10/706393   CofC

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Certificate
AUG 0 2 2006
of Correction

First Named Applicant: Flynn	)	Art Unit: 2651
	)	
Patent No.: 7,072,132	)	Examiner: Tzeng
	)	
Issued: July 4, 2006	)	HSJ920030231US1
	)	
For: SYSTEM AND METHOD FOR WRITING SERVO	)	July 5, 2006
TRACK IN SEALED HDD	)	750 B STREET, Suite 3120
	)	San Diego, CA 92101
	)	

CERTIFICATE OF CORRECTION - PATENT OFFICE MISTAKE

Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The Patent Office made a mistake in printing independent Claim 1 as issued in the subject patent, and correction is hereby requested.

Col. 8, line 2, "tow" should read --two--.

Enclosed is a page from the last amendment to Claim 1 recorded as being received by the Patent Office March 19, 2006, clearly showing that the patentee added the word "two", not the word "tow", to the claim.

Respectfully submitted,

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1189-21.CC

AUG 03 2006

1. (currently amended) A HDD comprising:

at least one write channel including at least one write gate; and

control circuitry controlling the write gate using write control bits to selectively enable

writing data bits associated with a servo pattern onto at least one disk, wherein the control circuitry

uses two bits of a ten bit parallel bus as write control bits to indicate whether the write gate should

enable writing one or more of the remaining eight bits of the bus to disk.

2. (original)   The HDD of Claim 1, wherein the control circuitry writes a servo pattern after the

HDD has been sealed.

3. (original)   The HDD of Claim 1, wherein the write channel is used during operation to write

user data to the disk.

4. (canceled).

5. (currently amended) ~~The HDD of Claim 1,~~ A HDD comprising:

at least one write channel including at least one write gate; and

control circuitry controlling the write gate using write control bits to selectively enable

writing data bits associated with a servo pattern onto at least one disk, wherein the control circuitry

uses four bits of an eight bit parallel bus as write control bits to indicate whether the write gate

should enable writing one or more of the remaining four bits of the bus to disk.

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